

10/550278

XA-10438

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

NISHIZAWA, Hirotaka et al.

Intl. Appln. No.: PCT/JP2004/013931

Intl. Filing Date: 24 September 2004

For: SEMICONDUCTOR DEVICE

\* \* \*

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Attn: Mail Stop PCT, DO/EO/US

Sir:

Pursuant to 37 C.F.R. § 1.56, and without any assertion as to materiality or prior art effect, the documents listed on the attached Form PTO-1449 are hereby cited.

The document on the attached List was cited in the specification, on pages 1-2, and its relevance is indicated therein.

Respectfully submitted,

MWS:sjk

Miles & Stockbridge P.C.  
1751 Pinnacle Drive, Suite 500  
McLean, Virginia 22102-3833  
(703) 903-9000

By: 

Mitchell W. Shapiro  
Reg. No. 31,568

September 23, 2005

FORM PTO-1449 INFORMATION DISCLOSURE STATEMENT LIST OF DOCUMENTS CITED BY APPLICANT				Atty. Docket No. XA-10438		Appln. No. <b>10/550278</b>	
				Applicant NISHIZAWA, Hirotaka et al.			
				Filing Date HEREWITH		Group	
U.S. PATENT DOCUMENTS							
Examiner Initial		Document Number	Date	Name	Class	Sub-class	Filing Date
	AA						
	AB						
	AC						
	AD						
FOREIGN PATENT DOCUMENTS							
Examiner Initial		Document Number	Date	Country	Class	Sub-class	Translation
	AE	2000-99215	04/07/00	Japan			Yes
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						
	AM						
	AN						
	AO						
	AP						
	AQ						
	AR						
	AS						
	AT						
OTHER (including author, title, date, pertinent pages, etc.)							
	AU						
Examiner				Date Considered			
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.							